



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Confirmation No. 8286
Masahiro ONO et al. : Docket No. 2002_0013A
Serial No. 10/030,739 : Group Art Unit 2814
Filed January 11, 2002 : Examiner Nathan W. Ha

CIRCUIT SUBSTRATE FOR PACKAGING
SEMICONDUCTOR DEVICE, METHOD
FOR PRODUCING THE SAME, AND METHOD
FOR PRODUCING SEMICONDUCTOR DEVICE
PACKAGE STRUCTURE USING THE SAME

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

2814
#4
9603

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement of August 12, 2003, Applicants hereby elect Group I, which is drawn to a semiconductor device and is embodied by claims 1-2 and 10.

Having made the required election, a full examination on the merits of the elected group is requested.

Respectfully submitted,

Masahiro ONO et al.

By:

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